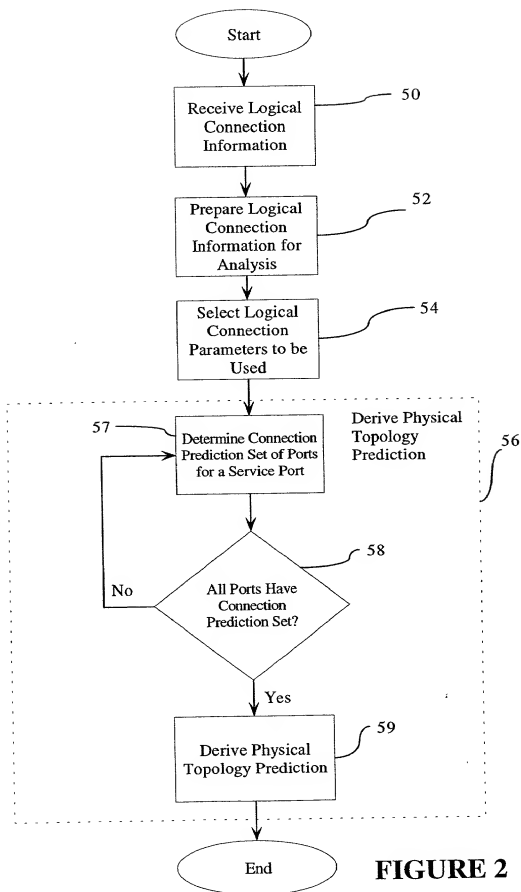


**FIGURE 1**

**FIGURE 2**

Consider Port N  
with M CTPs

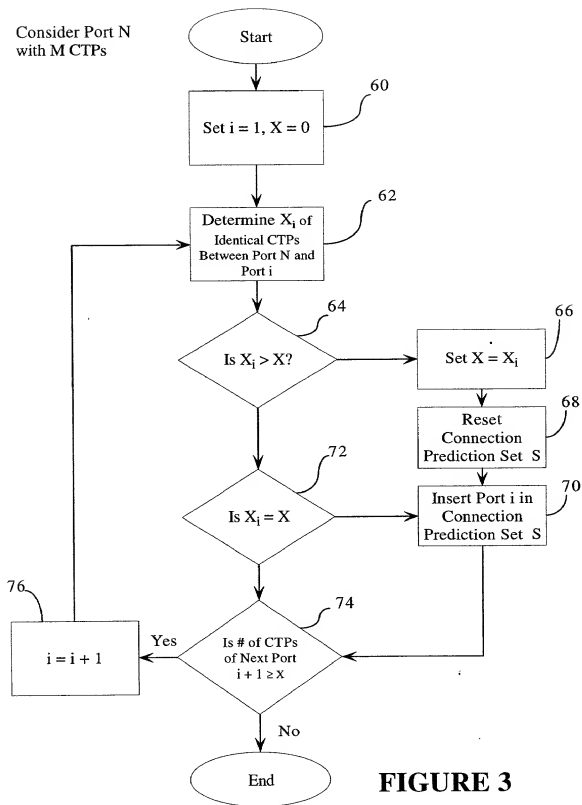
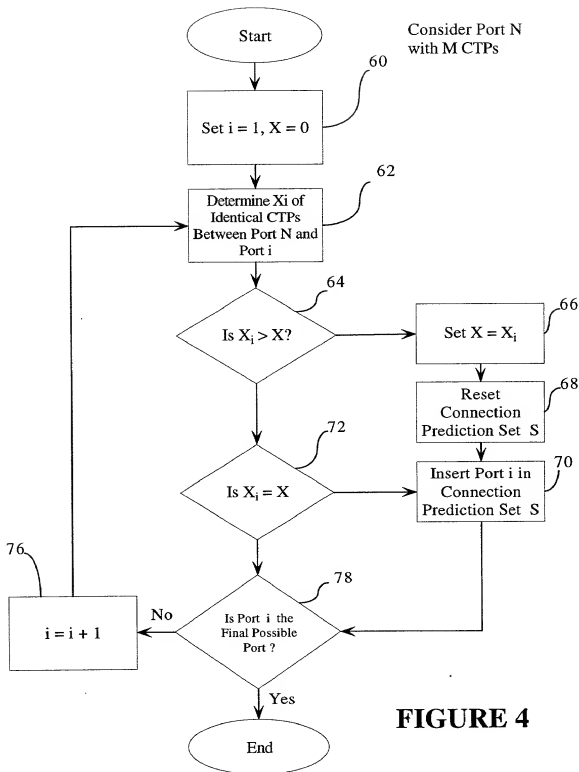
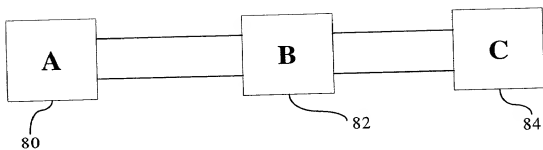


FIGURE 3





**FIGURE 5A**

VC12

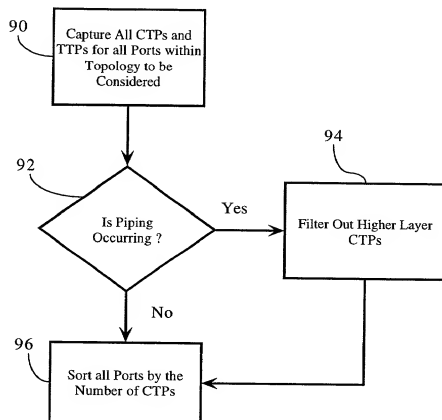


**FIGURE 5B**

VC4



**FIGURE 5C**

**FIGURE 6**